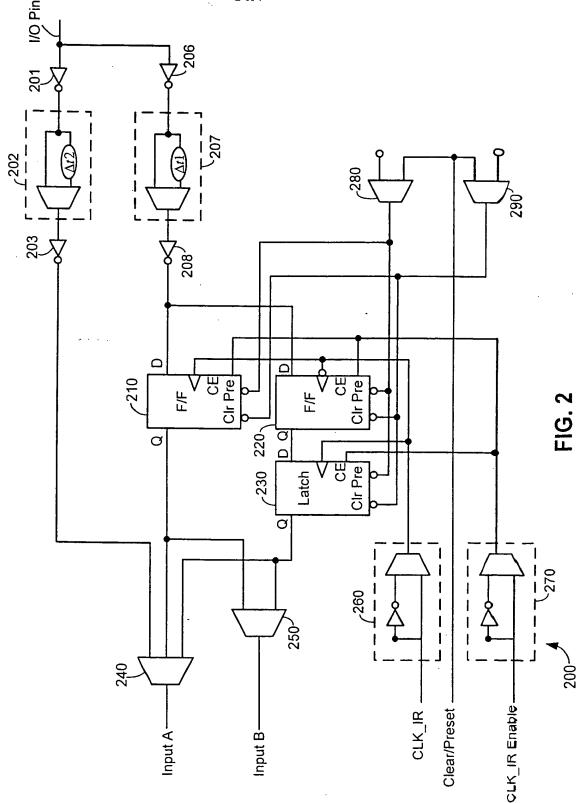
Title: Programmable I/O Element Circuit for High Speed Logic Devices AUG 1 7 2004 Inventors: Nguyen et al. Application No.: 10/685,355 1 of 7 -120 Logic Array C4401 **OE** Register OE-Q OE Out 410 430 OE Register Q 400 I/O Pin 150 3401 Output Register 110 305 Output A. Q 310 330 **Output Register** Output B. Input Register D Q 240 320 Q 2,10 250 0<u>08</u> Input A Input B Input Register Input Latch 200 230

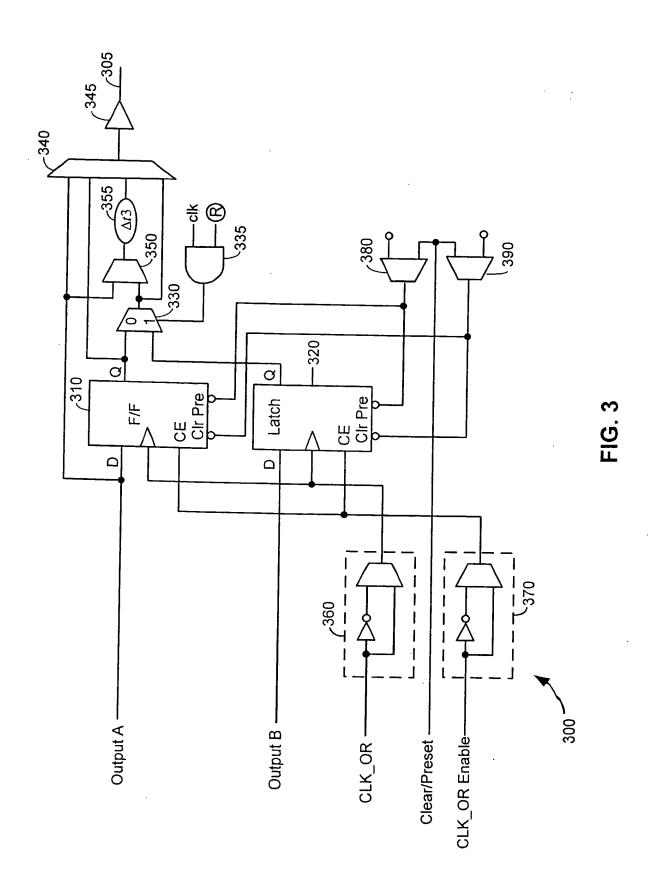
FIG. 1

100

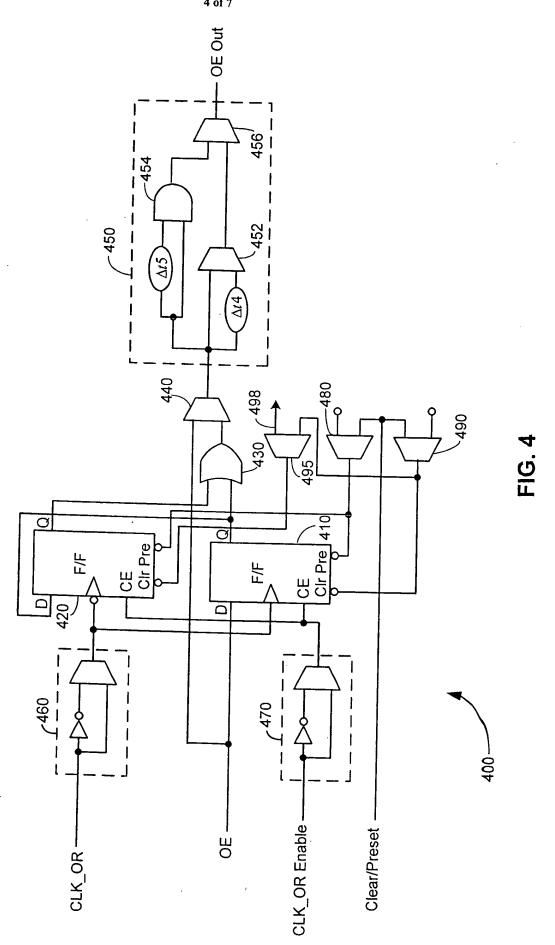
Title: Programmable I/O Element Circuit for High Speed
Logic Devices
Inventors: Nguyen et al.
Application No.: 10/685,355
2 of 7



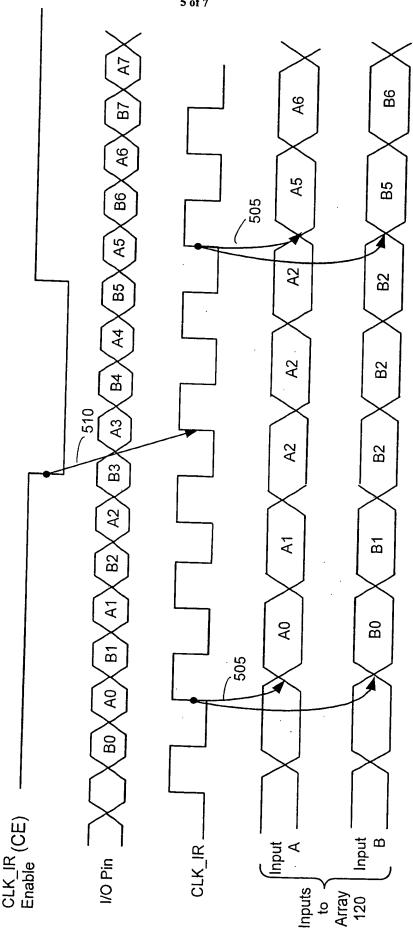
Title: Programmable I/O Element Circuit for High Speed
Logic Devices
Inventors: Nguyen et al.
Application No.: 10/685,355
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Title: Programmable I/O Element Circuit for High Speed
Logic Devices
Inventors: Nguyen et al.
Application No.: 10/685,355
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Logic Devices
Logic Devices
Inventors: Nguyen et al.
Application No.: 10/685,355
5 of 7



Title: Programmable I/O Element Circuit for High Speed Logic Devices

Inventors: Nguyen et al. Application No.: 10/685,355 6 of 7

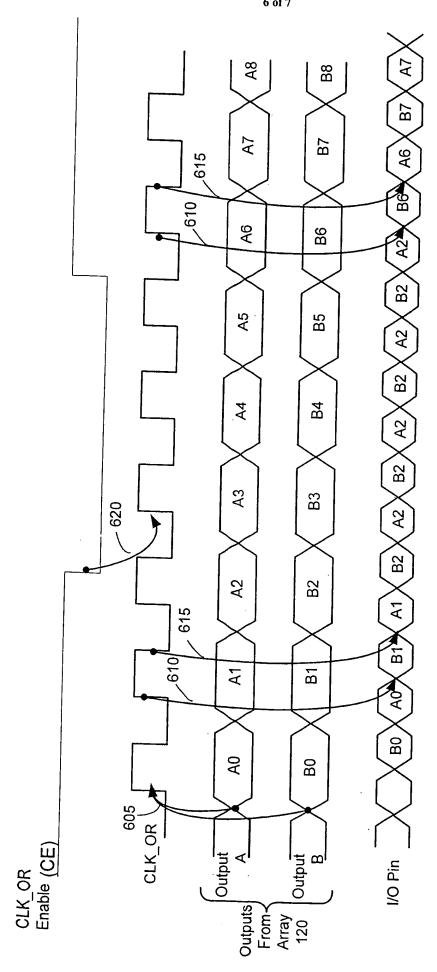


FIG. 6

Title: Programmable I/O Element Circuit for High Speed
Logic Devices
Inventors: Nguyen et al.
Application No.: 10/685,355
of 7

